

Motorola Semiconductor Technical Data

Addendum to **MC68HC908GR8**

This addendum provides changes to the MC68HC908GR8 Technical Data Book Rev 4 (Motorola document order number MC68HC908GR8/D).

Incorrect Register Addresses

Incorrect addresses:

Address of Interrupt Status Register 1 (INT1) should be \$FE04, not \$FE09

Address of Interrupt Status Register 2 (INT2) should be \$FE05, not \$FE0A

Address of Interrupt Status Register 3 (INT3) should be \$FE06, not \$FE0B

Locations:

In 4th, 5th and 6th bullets of page 36, Memory Map section.

Near bottom of memory map on page 37, Memory Map section.

In bottom two registers on page 45 and top register on page 46, Memory Map section.

In last three registers on page 274, SIM section.



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MC68HC908GR8AD2/D